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Date: June 20, 2001

Docket No.: 0020-4878P

Assistant Commissioner for Patents  
Box PATENT APPLICATION  
Washington, D.C. 20231

Sir:

Transmitted herewith for filing is the patent application of

Inventor(s): KOYAMA, Eiji

For: SEMICONDUCTOR INTEGRATED CIRCUIT

Enclosed are:

- X  A specification consisting of  29  pages
- X   6  sheet(s) of  FORMAL  drawings
- X  An assignment of the invention - \$40.00 Recording Fee
- X  Certified copy of Priority Document(s)
- X  Executed Declaration  X  Original   Photocopy
- Applicant claims small entity status in accordance with 37 CFR 1.27
- Application Data Sheet in accordance with 37 C.F.R. 1.76

☐ Preliminary Amendment

☒ Information Disclosure Statement, PTO-1449 and reference(s)

☐ Other \_\_\_\_\_

☐ Applicant requests early publication

The filing fee has been calculated as shown below:

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## SMALL ENTITY

FOR	NO. FILED	NO. EXTRA	RATE	FEE		RATE	FEE
BASIC FEE	***** ***** *****	***** ***** *****	***** ***** *****	\$710.00	or	**** **** ****	\$355.00
TOTAL CLAIMS	6 - 20 =	0	x18 = \$	0.00	or	x 9 = \$	0.00
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MULTIPLE DEPENDENT CLAIM PRESENTED <u>no</u>			+270 = \$	0.00	or	+135 = \$	0.00
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